

Please delete the paragraph beginning on page 10, line 14, and substitute therefore the following paragraph:

Q3 Referring to Fig. 11 another embodiment illustrates a fuse, without the voltage divider configuration used in Figs. 6-10, providing a parameter value to the integrated circuit die mounted on the package. In the particular embodiment, a signal is provided that selectively enables error correcting code (ECC) according to the state of the fuse. The ECC signal may be used, e.g., for an on-board cache for a microprocessor. An internal pull-up is required in the processor to specify a value if the fuse is not cut.

In the Claims

Please amend claim 14 as follows:

- Q4 14. (Amended) The package as recited in claim 2 wherein the package includes at least one pair of programmable elements, the one pair including the one one-time programmable element and a second one-time programmable element, the second one-time programmable element having a first and second end, the first end of the second one-time programmable element coupled to a second power supply voltage node and the second end of the second one-time programmable element being coupled to the second end of the first one-time programmable element.

REMARKS

Applicants note that the amendments made to the specification correct minor deficiencies, and the amendment to claim 14 broadens the claim in order to recite the claim with the scope to which applicants believe they are entitled.

Examination on the merits is respectfully requested. If the Examiner has any questions, Applicants respectfully request that the Examiner contact the undersigned at the telephone number indicated below.

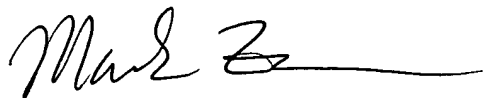
**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on the date shown below.

  
Mark Zagorin

10/12/07  
Date

Respectfully submitted,



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MARKED-UP COPY OF REPLACED PARAGRAPHS OF SPECIFICATION IN  
ACCORDANCE WITH 37 C.F.R. § 121(b)(iii)

Fig. 1 shows a prior art approach [to] for providing voltage and frequency settings.

Each of the fuses 301-304 and 315-318 include a fusible link coupling each end of the fuse formed by the metal trace or other suitable conductor. Referring to Fig. 3B, the programming shown provides a binary setting of [1-0-0-1] 1-0-1-0 as the value of the fuses, where 1 is Vcc and 0 is Vss. That is, fuses 315 and 317 are blown causing vias 310 and 312 to be coupled only to Vcc. Fuses 302 and 304 are also blown causing vias 311 and 313 to be only coupled to Vss.

Referring to Fig. 11 another embodiment illustrates a fuse, without the voltage divider configuration used in Figs. 6-10, providing a parameter value to the integrated circuit die mounted on the package. In the particular embodiment, a signal is provided [to] that selectively enables error correcting code (ECC) according to the state of the fuse. The ECC signal may be used, e.g., for an on-board cache for a microprocessor. An internal pull-up is required in the processor to specify a value if the fuse is not cut.

MARKED-UP COPY OF AMENDED CLAIMS IN ACCORDANCE WITH  
37 C.F.R. § 121(c)(ii)

14. (Amended) The package as recited in claim 2 wherein the package includes at least one pair of programmable elements, the one pair including the one one-time programmable element and a second one-time programmable element, the second one-time programmable element having a first and second end, the first end of the second one-time programmable element coupled to a second power supply voltage node and the second end of the second one-time programmable element being coupled [through an internal package node] to the second end of the first [-]one-time programmable element.